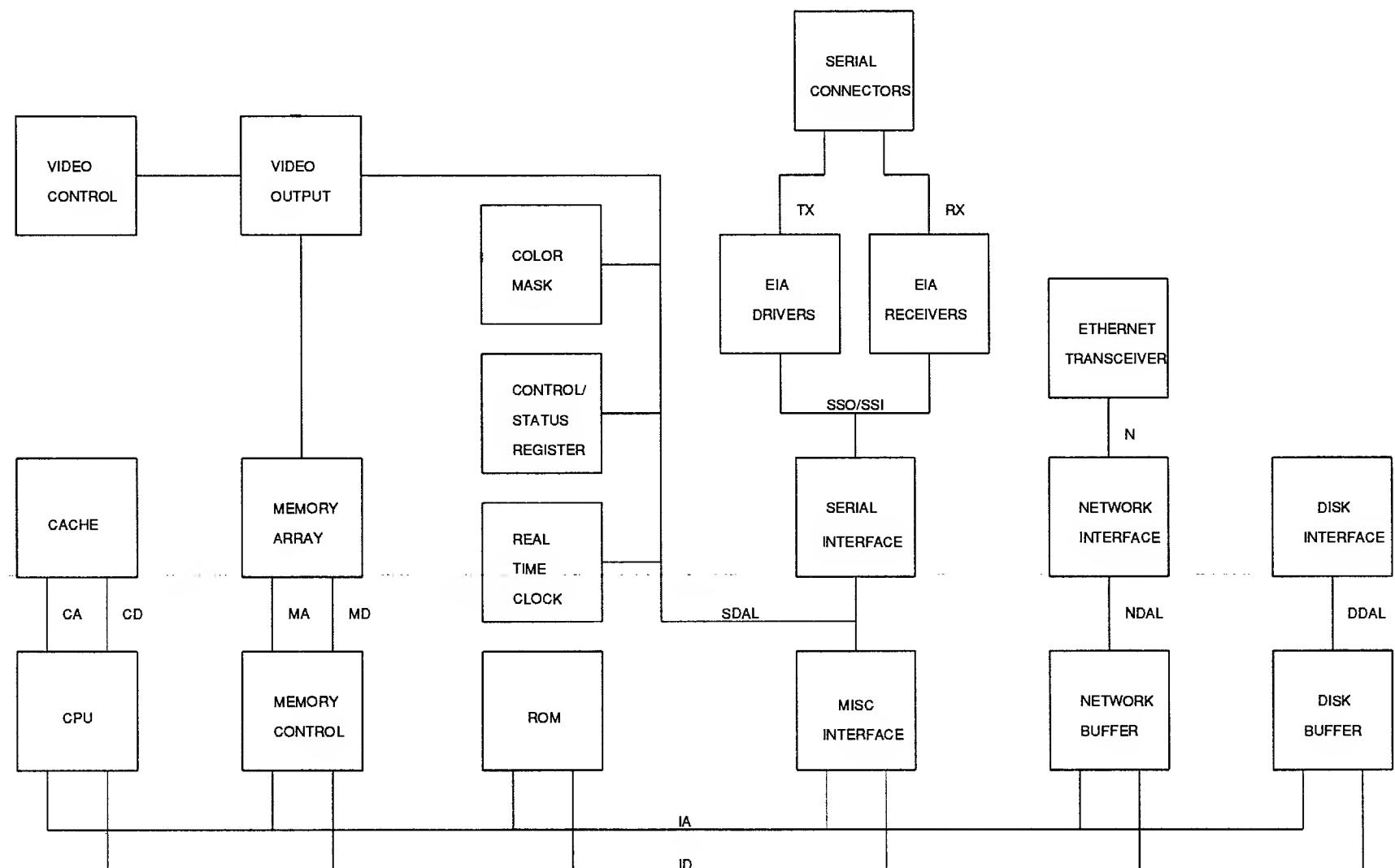


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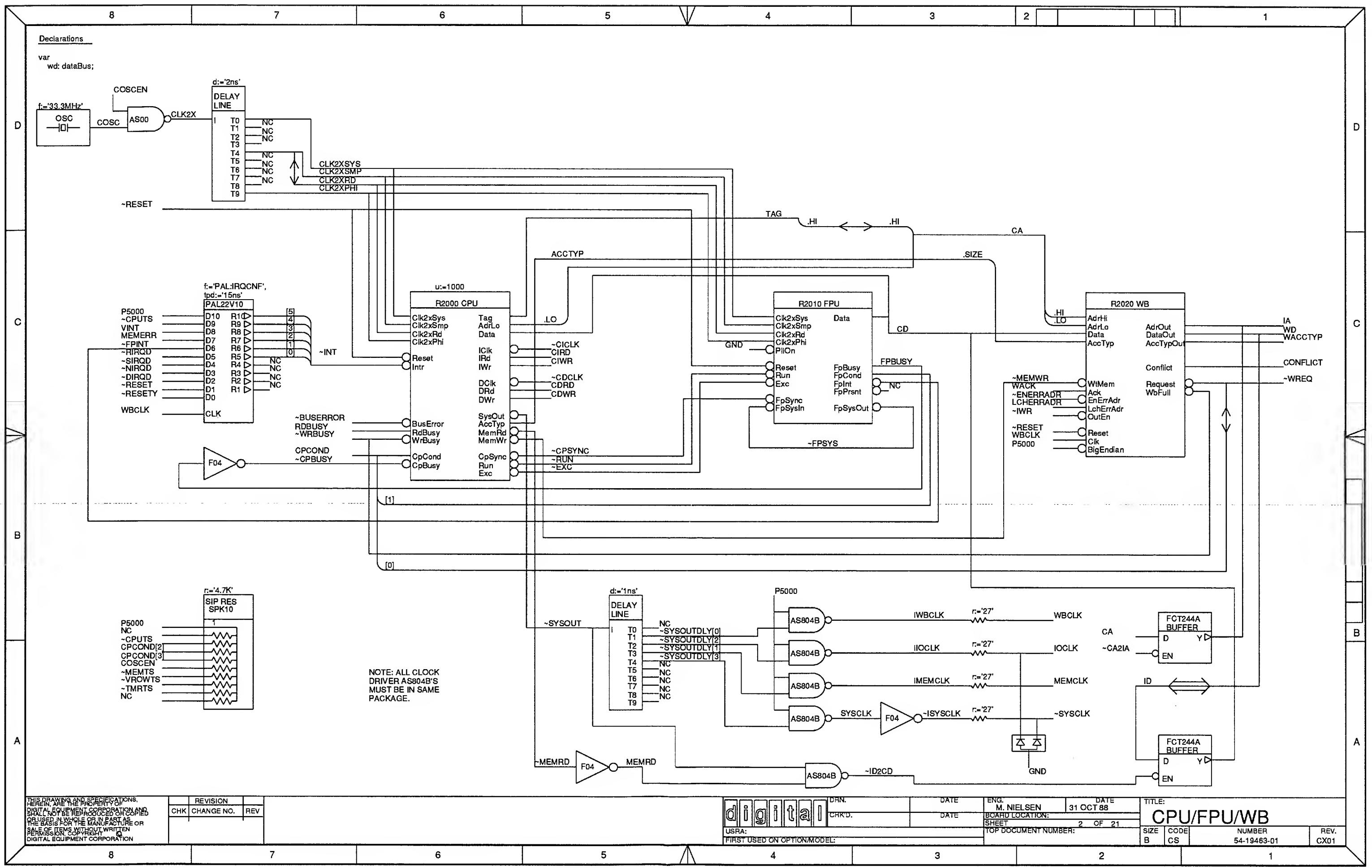
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FIRST USED ON OPTION/MODEL:

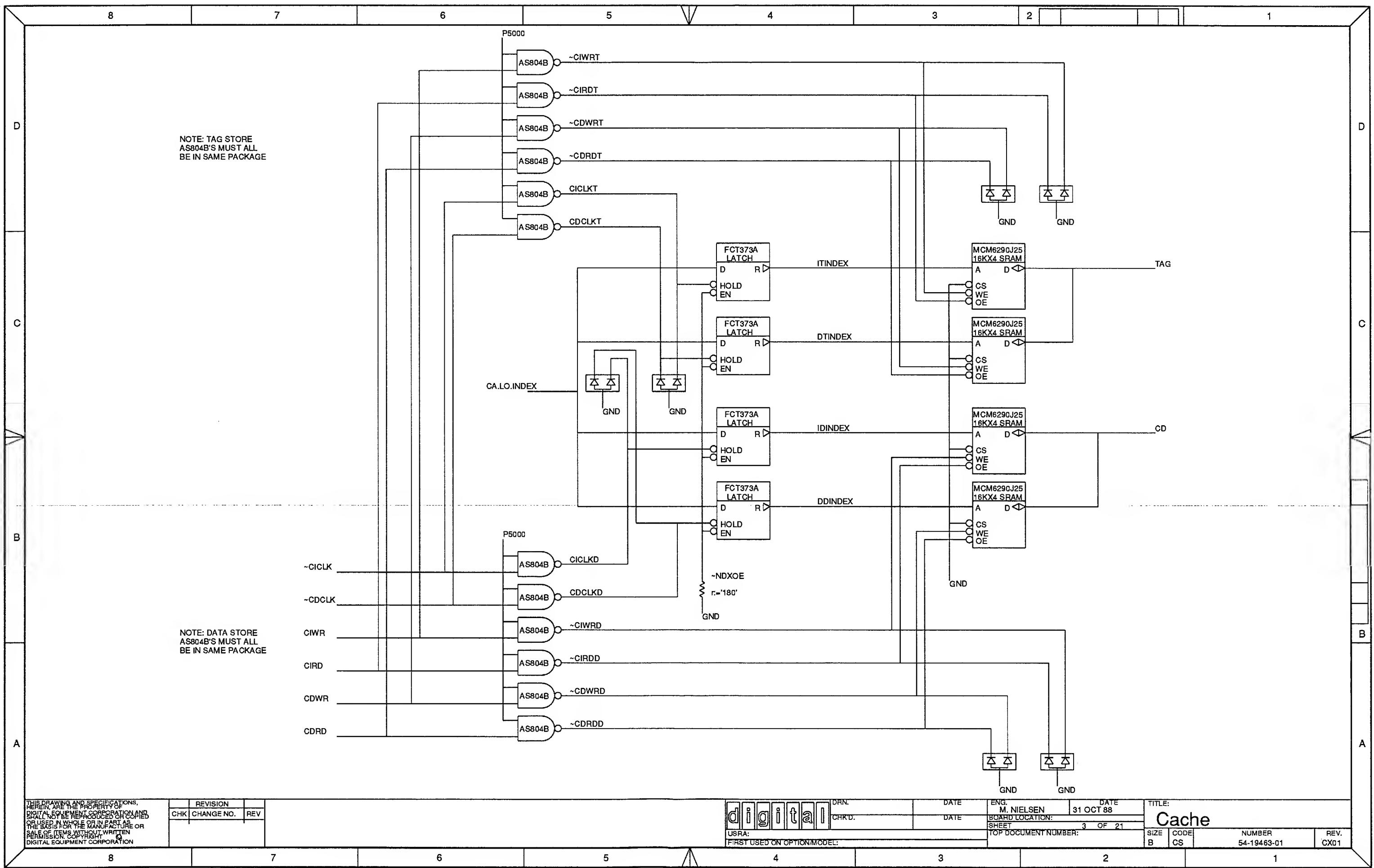
N. ID.	DATE	ENG. M. NIELSEN	DATE 31 OCT 88
	DATE	BOARD LOCATION:	
		SHEET	1 OF 21
		TOP DOCUMENT NUMBER:	

PMAX System Module

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Declarations

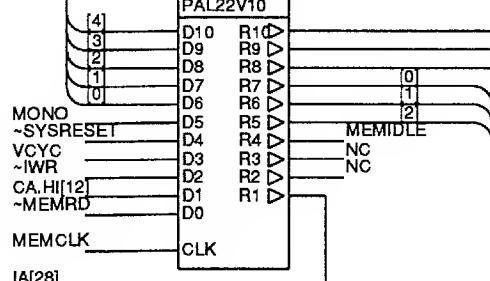
```

type
memAdr = record
  reserved: bits(7);
  bank: bits(3);
  row, col: bits(10);
  byte: bits(2);
end;
var
  mra, mwa: memAdr;
  mc: bits(10);
  i, j: cardinal;
  vrr, vwr, vc, vs: bits(2);

```

f:='PAL:MUXCTL',
tpd:='15ns',
r1class:=inputPin

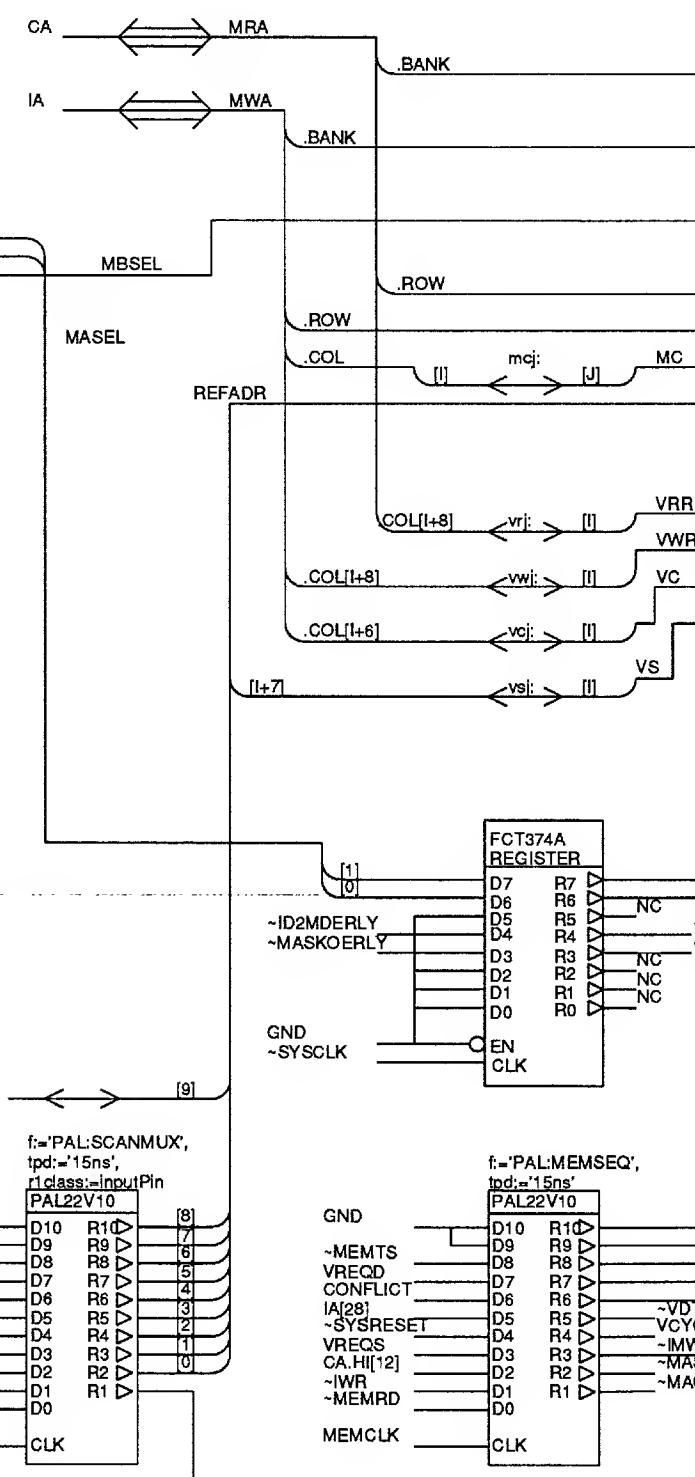
PAL22V10

Code

```

for i := 0 to 9 do begin
  if i = 7 then
    j := i+1
  else if i = 8 then
    j := i-1
  else
    j := i;
  mci;
end;
for i := 0 to 1 do begin
  vrj;
  vwj;
  vcj;
  vsj;
end;

```



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CHK	REVISION	CHANGE NO.	REV

digital		DRN.	DATE	ENG.	DATE	TITLE:
USRA:		CRK'D.		M. NIELSEN	31 OCT 88	Memory Control
FIRST USED ON OPTION/MODEL:						
TOP DOCUMENT NUMBER:						
SHEET		4	OF	21		
SIZE	CODE					
B	CS					
NUMBER		54-19463-01				
REV.		CX01				

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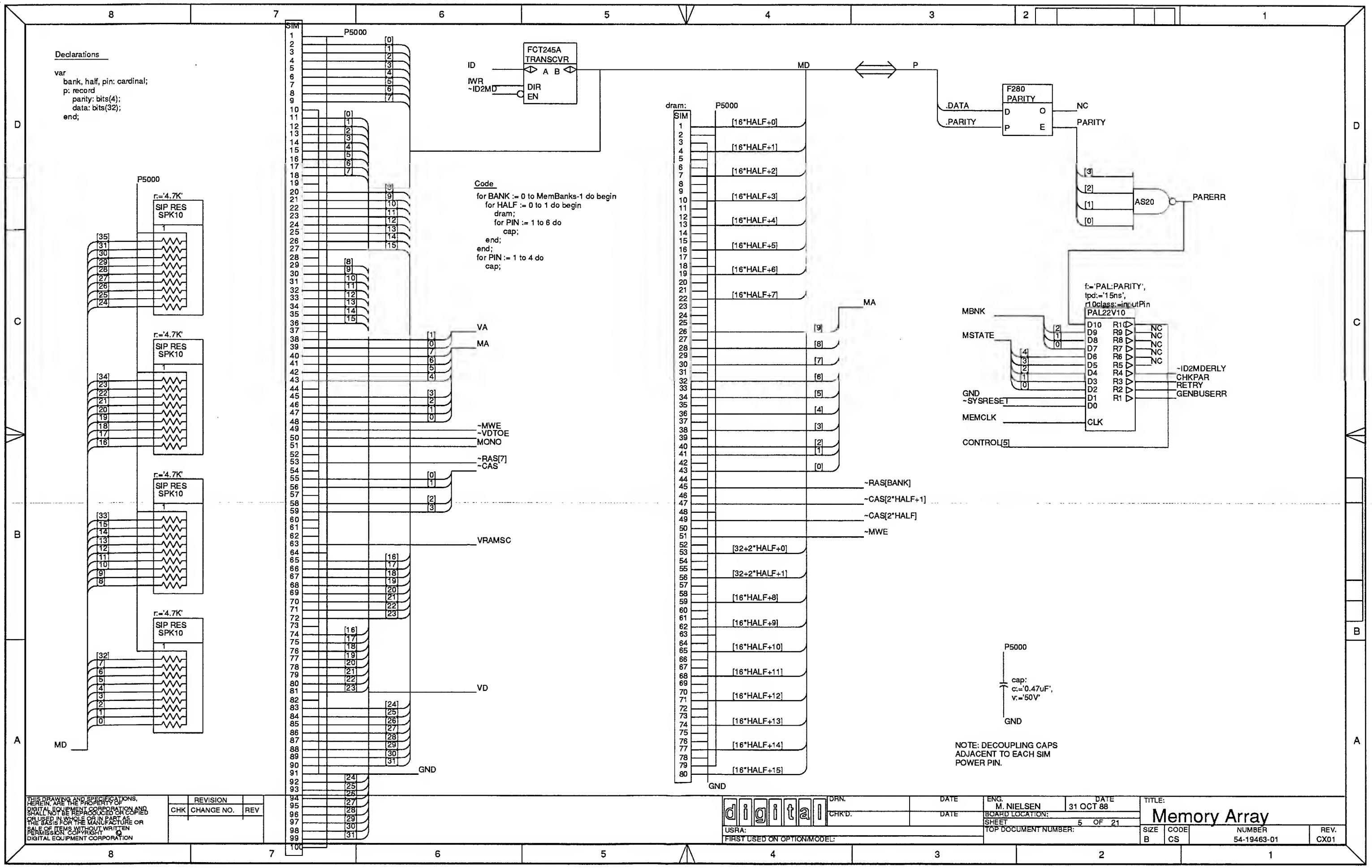
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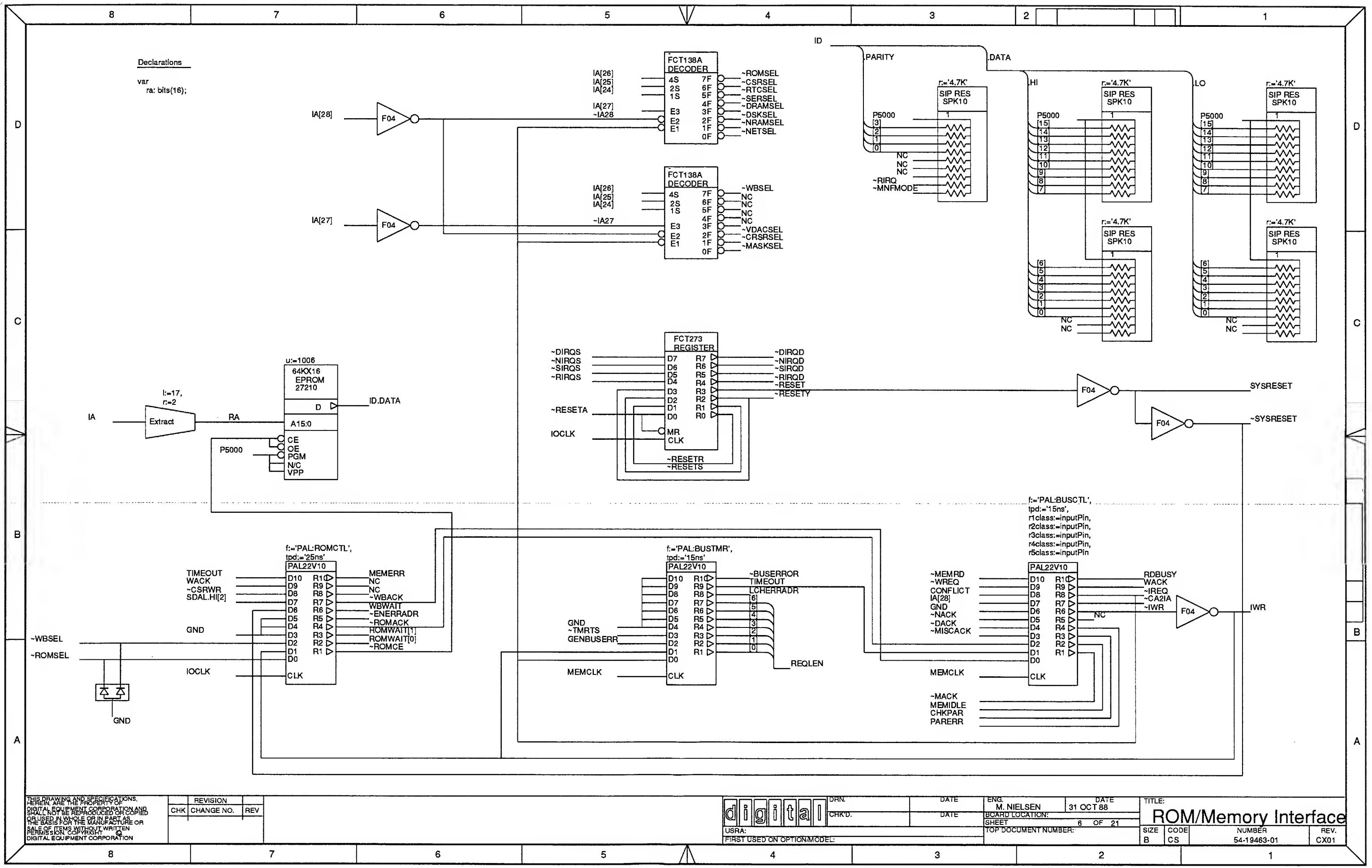
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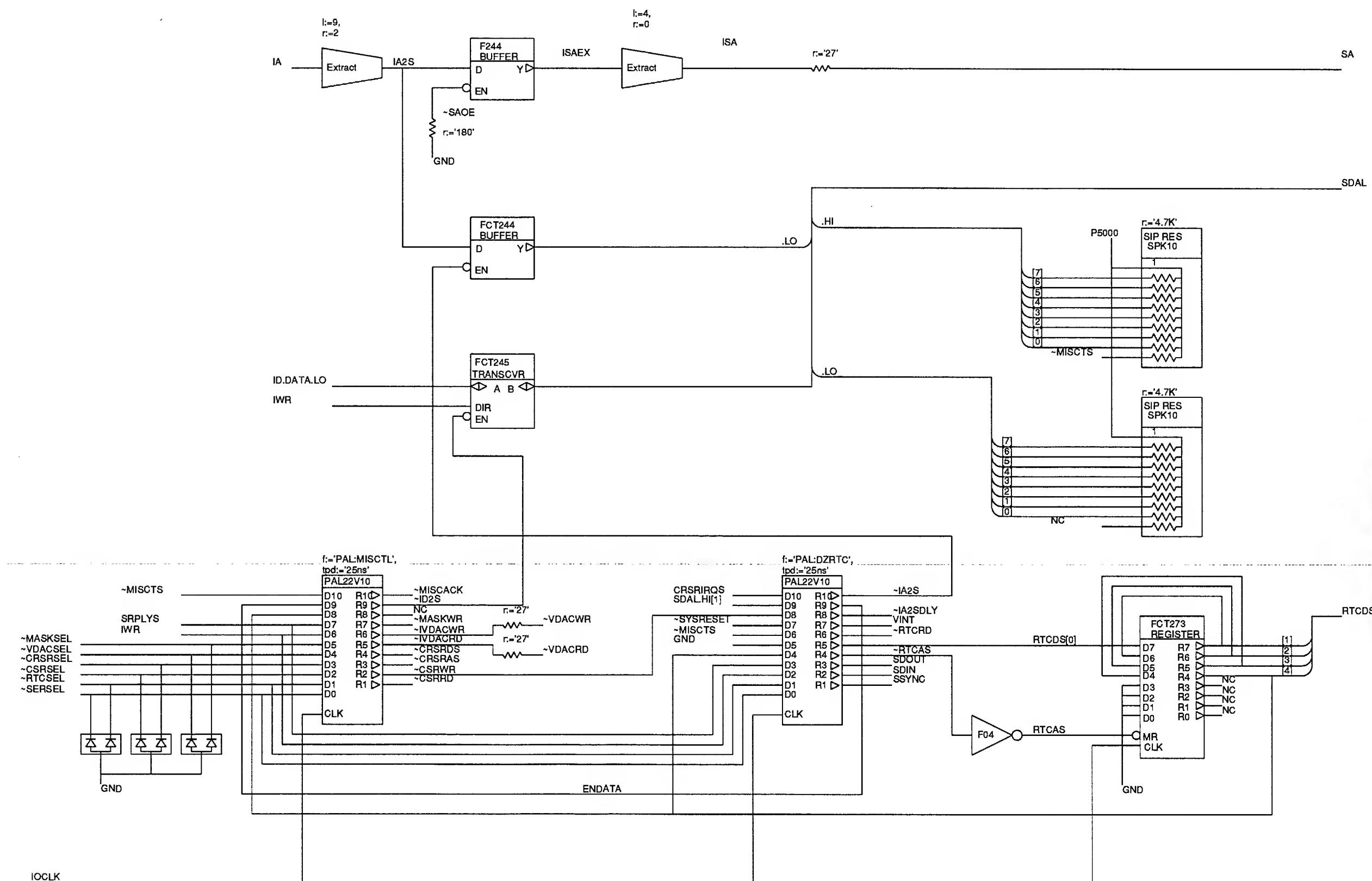
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CHK	REVISION	CHANGE NO.	REV

digital		DRN. _____	DATE _____	ENG. M. NIELSEN	DATE 31 OCT 88	TITLE: ROM/Memory Interface
USRA:	CHKD:	DATE _____	BOARD LOCATION: SHEET 6 OF 21	SHEET _____	TOP DOCUMENT NUMBER: 54-19463-01	SIZE B CODE CS REV. CX01
FIRST USED ON OPTION/MODEL:						

8 7 6 5 4 3 2 1

Declarations
var
ia2s: bits(8);



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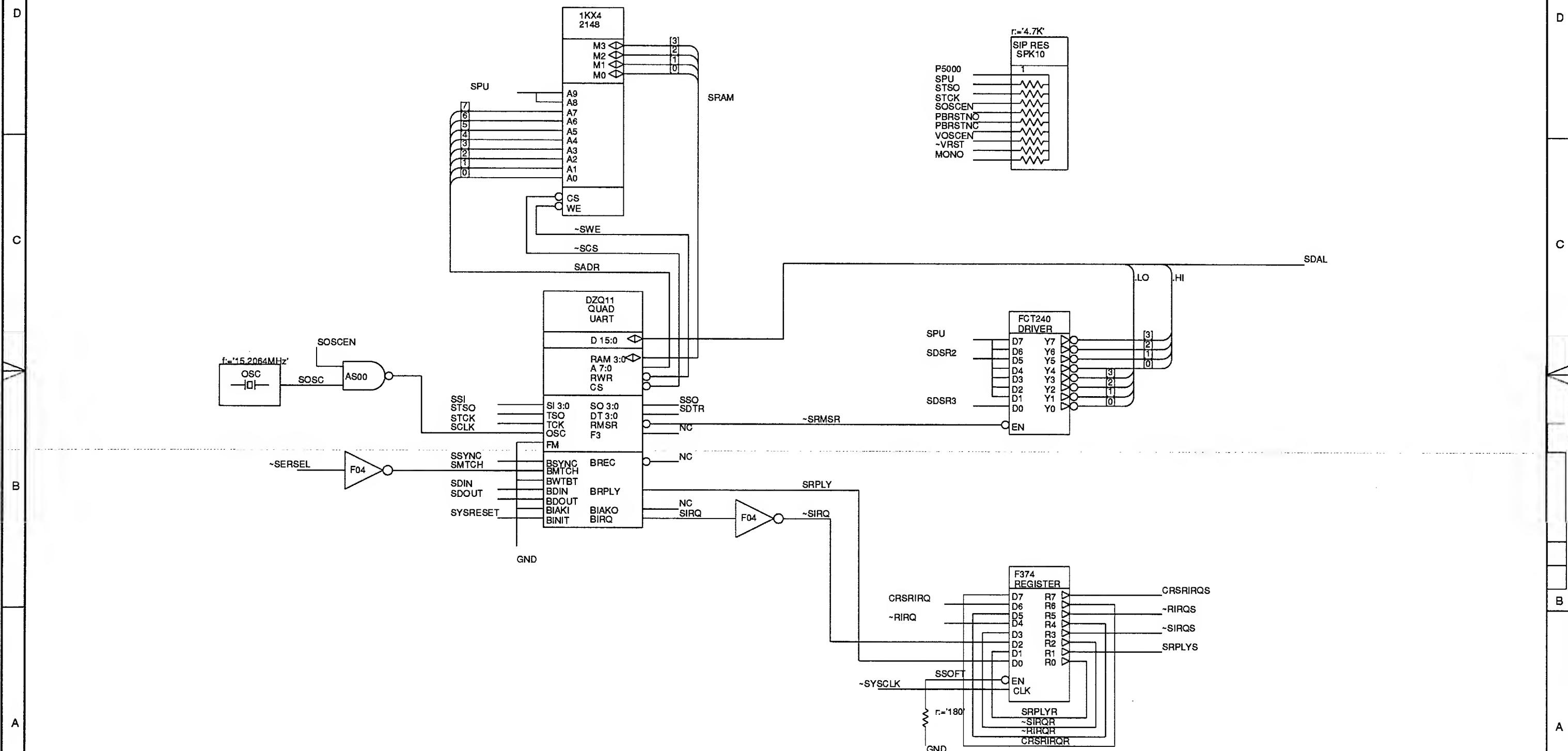
digital
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FIRST USED ON OPTION/MODEL:

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CHKD. DATE
SHEET 7 OF 21
TOP DOCUMENT NUMBER:

ENG. M. NIELSEN 31 OCT 88
BOARD LOCATION:
NUMBER 54-19463-01
SIZE B CODE CS REV. CX01

Misc Interface

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CHK	REVISION	CHANGE NO.	REV

digital	DRN. / CHK'D.	DATE	ENG. M. NIELSEN	DATE 31 OCT 88	TITLE: Serial Interface
USRA: FIRST USED ON OPTION/MODEL:		SHEET 8 OF 21	BOARD LOCATION:		
			TOP DOCUMENT NUMBER:		

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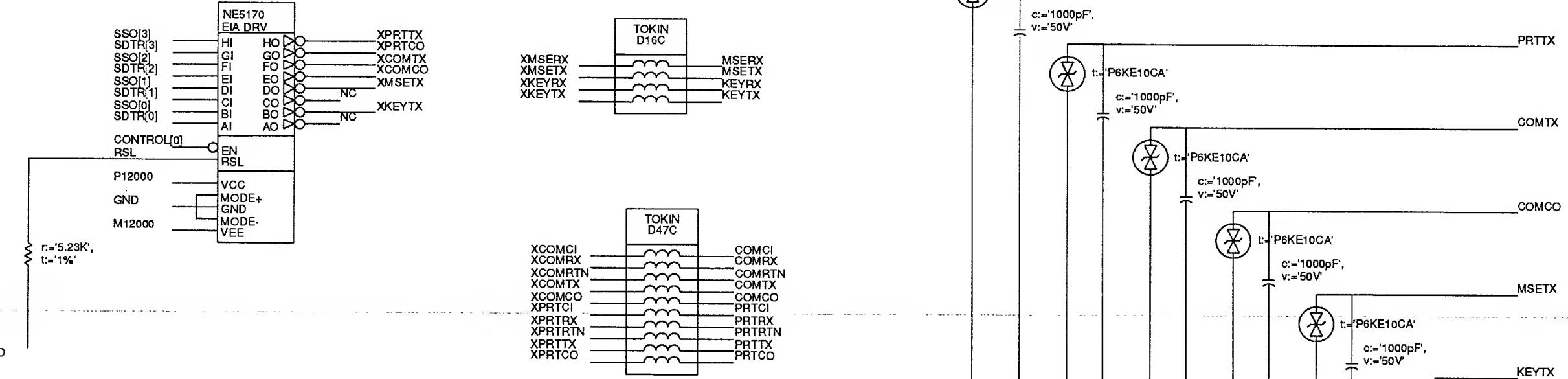
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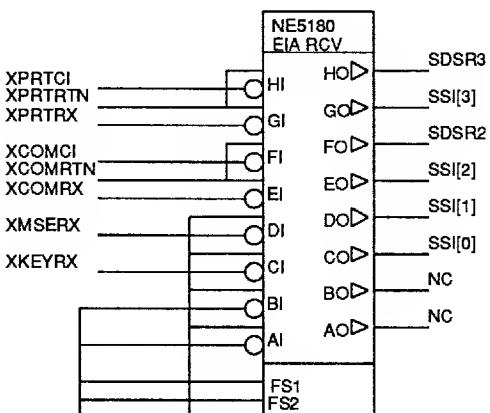
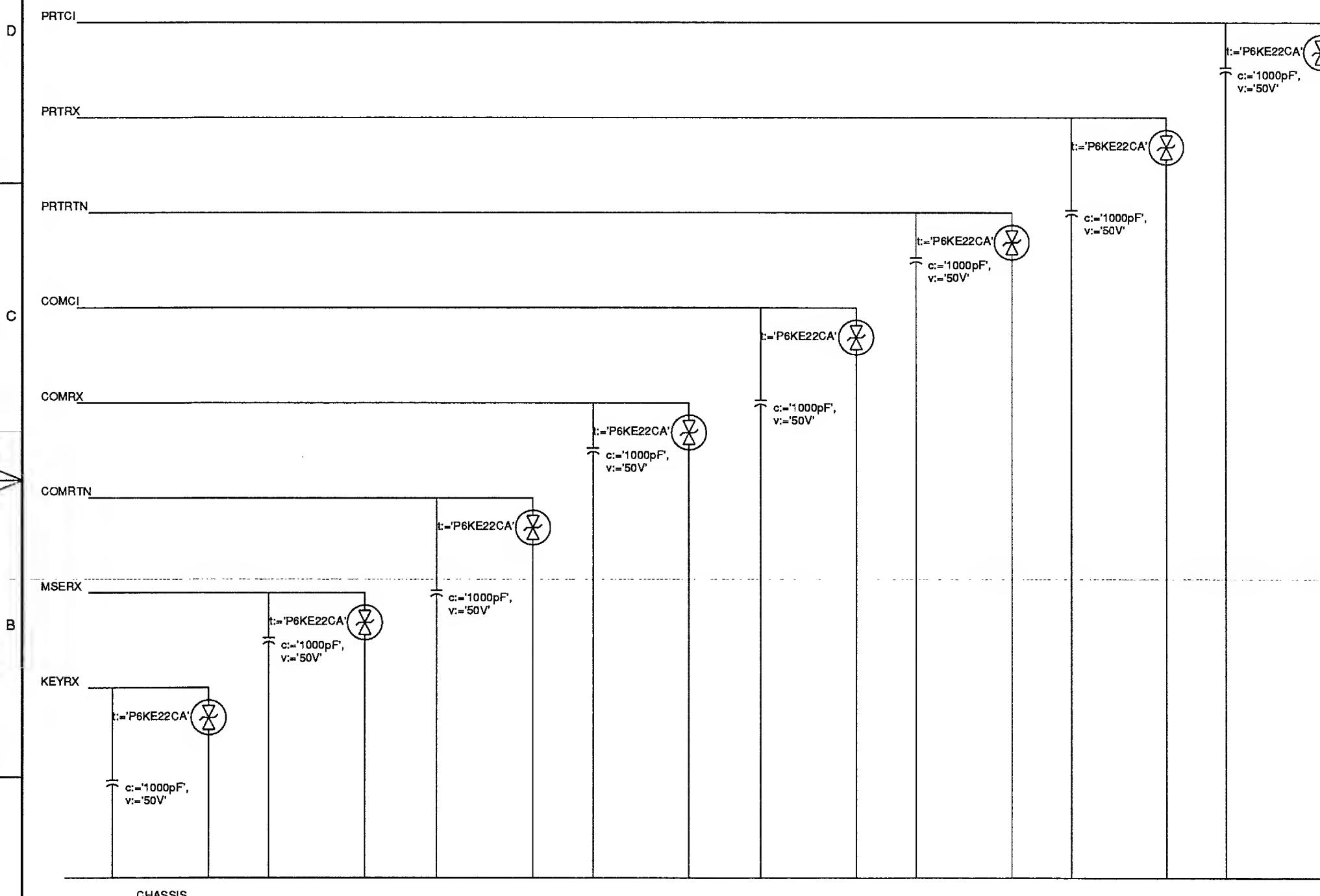
NOTE: TRANSORBS AND FILTERS MUST BE
IMMEDIATELY ADJACENT TO SERIAL LINE
CONNECTORS.

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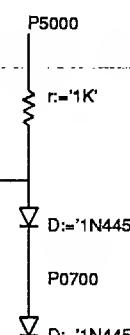
CHK	REVISION	CHANGE NO.	REV

digital	DRN.	DATE	ENG.	M. NIELSEN	DATE	BOARD LOCATION: SHEET 9 OF 21	TITLE: EIA Drivers
	CRR'D.	DATE					
USRA: FIRST USED ON OPTION/MODEL:						TOP DOCUMENT NUMBER:	SIZE B CODE CS NUMBER 54-19463-01 REV. CX01

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M. NIELSEN
31 OCT 88

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TITLE:
EIA Receivers

SIZE	CODE	NUMBER
B	CS	54-19463-01
REV.		CX01

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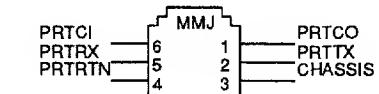
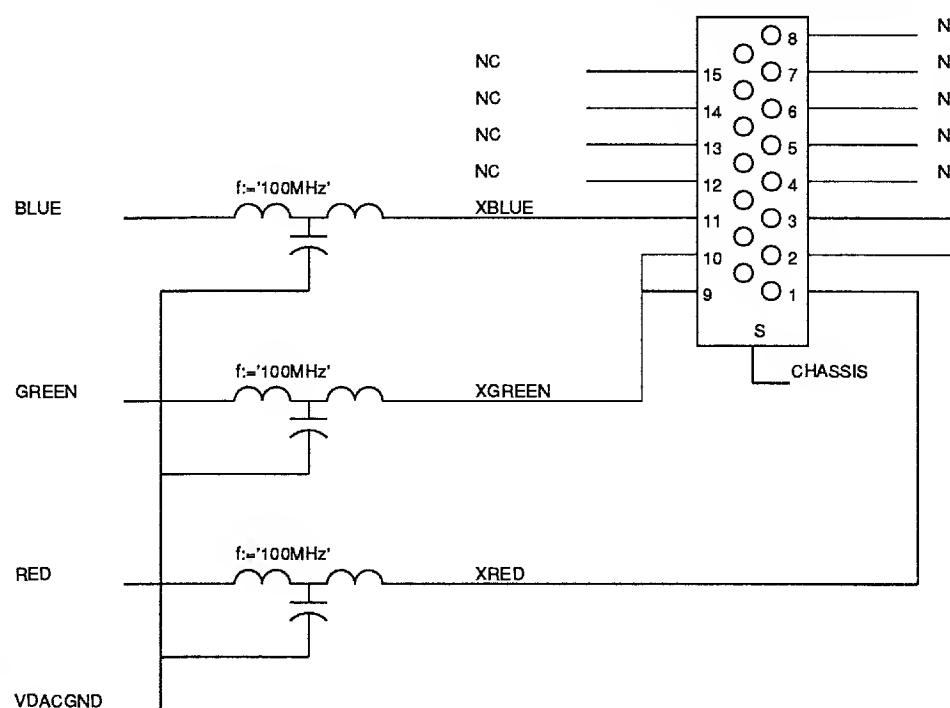
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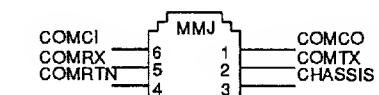
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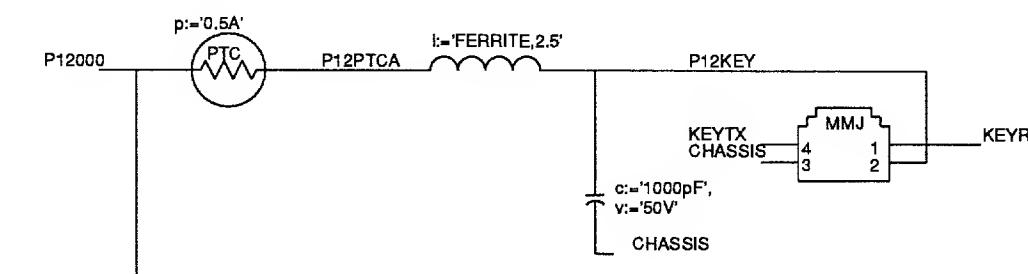
Video Connector



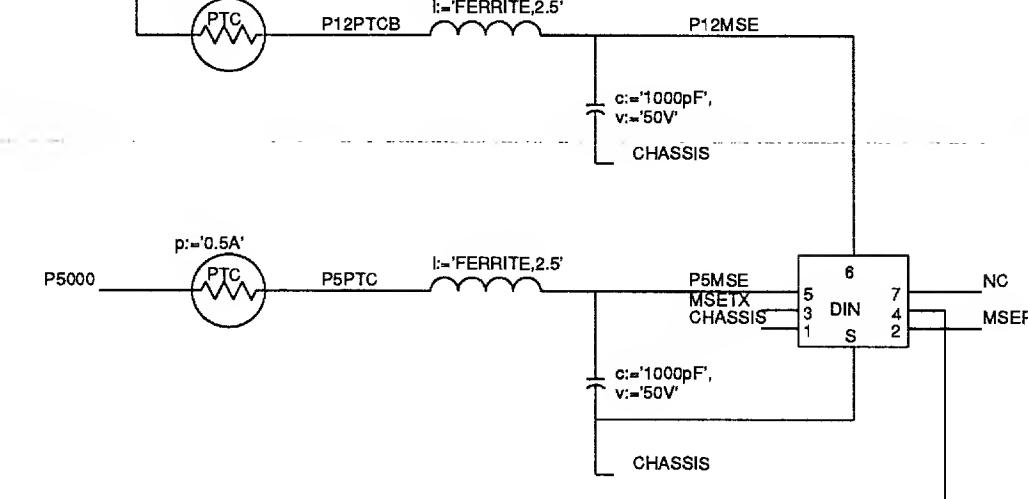
Printer Connector



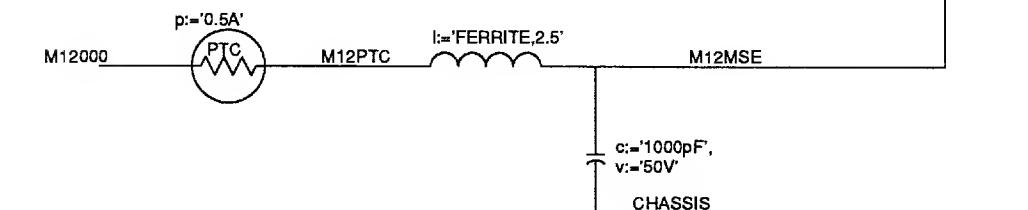
Comm Connector



Keyboard Connector



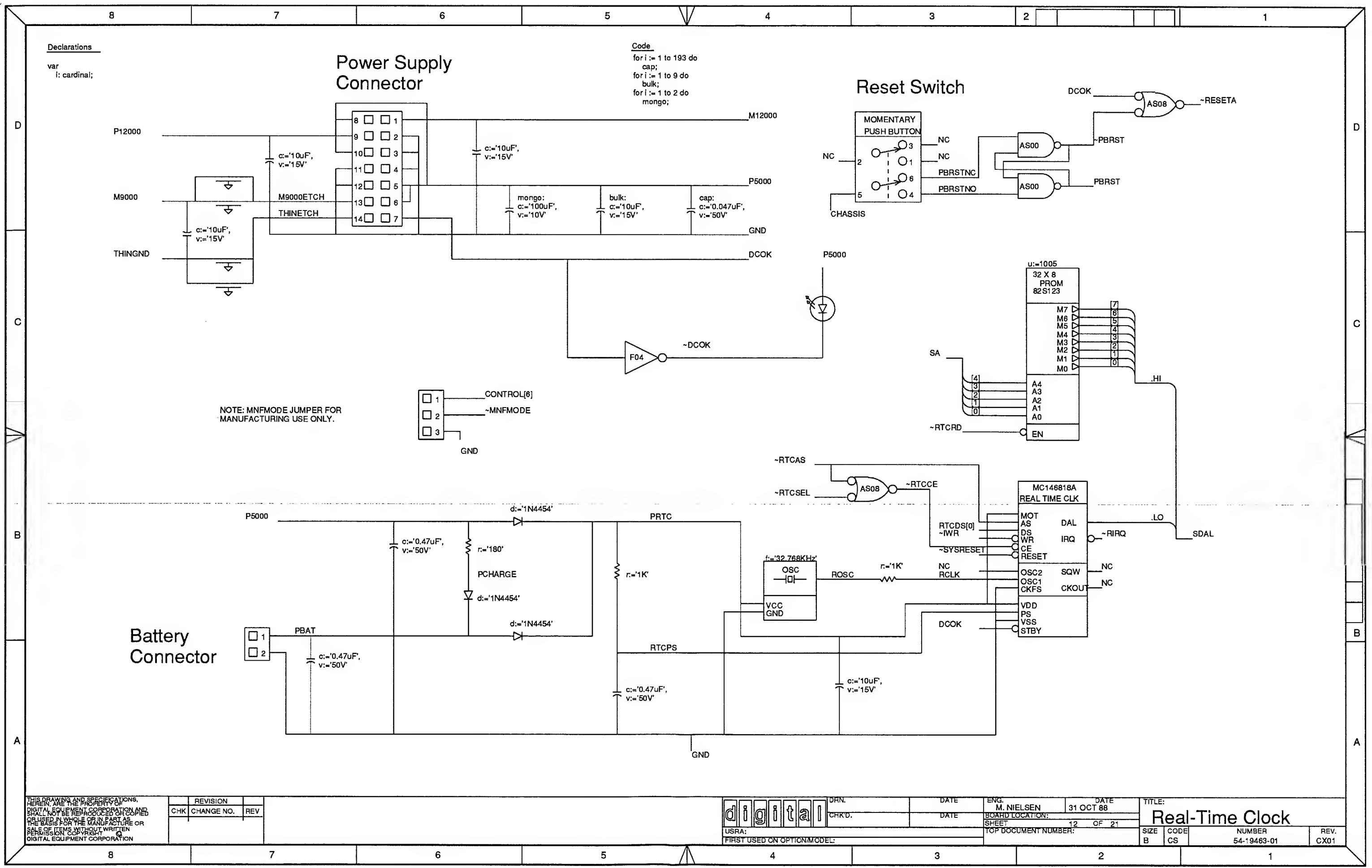
Mouse Connector

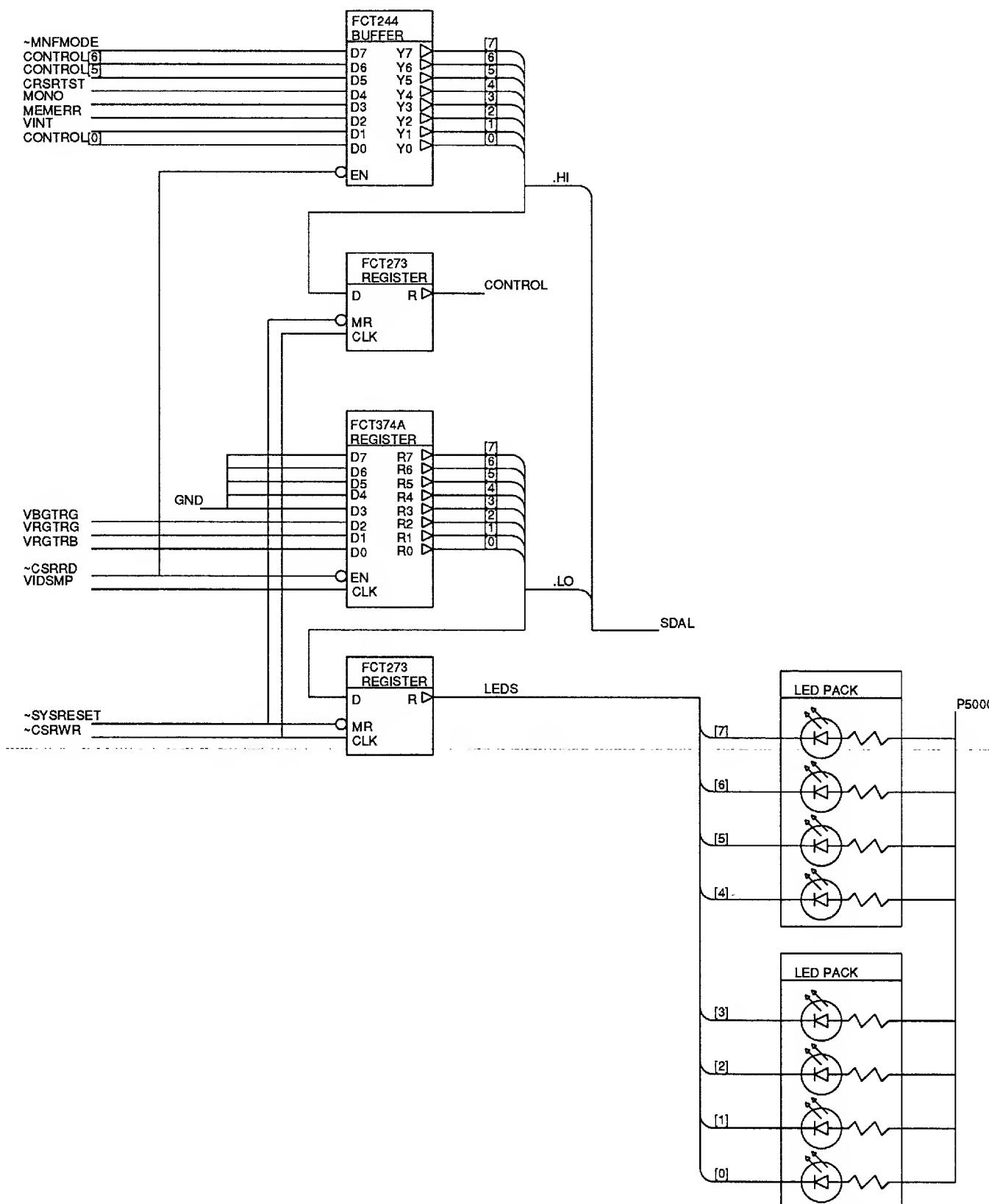


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CHK	REVISION	CHANGE NO.	REV

digital	DRN:	DATE	ENG. M. NIELSEN	DATE 31 OCT 88	TITLE: Serial Connectors
USRA:	CHK'D.	DATE	BOARD LOCATION:		
FIRST USED ON OPTION/MODEL:			SHEET 11 OF 21		
			TOP DOCUMENT NUMBER:		





**NOTE: LEDS ILLUMINATED
DURING/AFTER RESET ASSERTION.**

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TITLE:			
<u>Control/Status Register</u>			
SIZE	CODE	NUMBER	REV.
B	CS	54-19463-01	CX01

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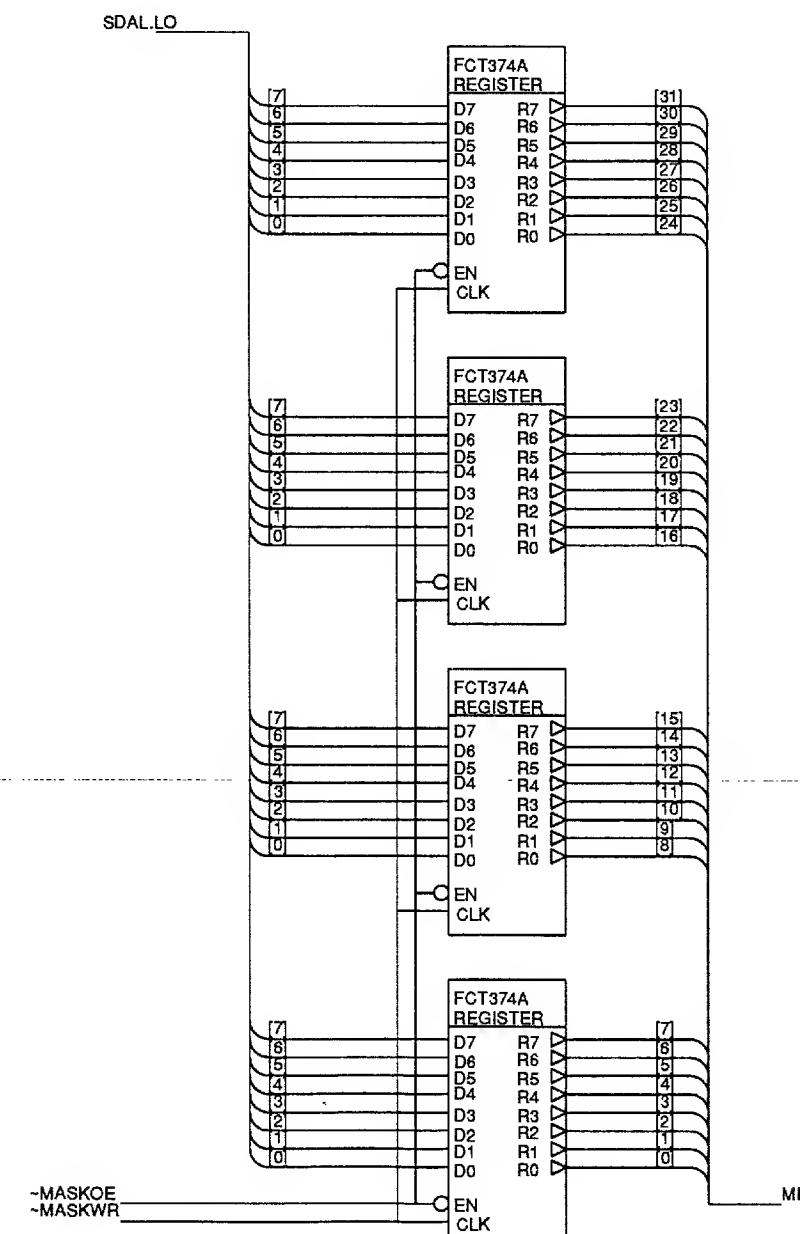
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CHK	REVISION	CHANGE NO.	REV

digital	DRN.	DATE	ENG.	DATE	TITLE:
USRA:	CHKD.	DATE	M. NIELSEN	31 OCT 88	Color Plane Mask
FIRST USED ON OPTION/MODEL:	SHEET	14 OF 21	BOARD LOCATION:		SIZE: B CODE: CS NUMBER: 54-19463-01 REV. CX01
	TOP DOCUMENT NUMBER:				

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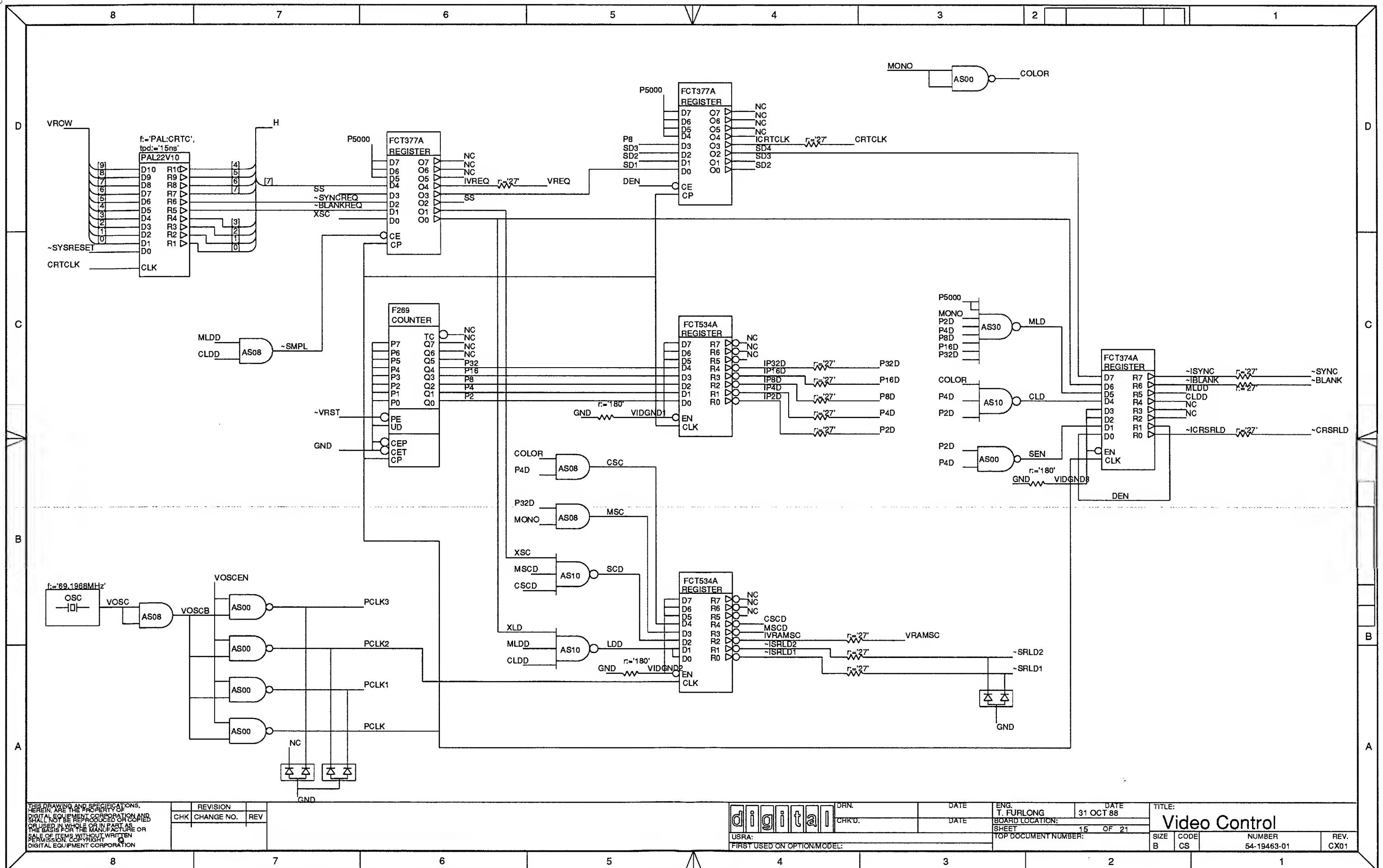
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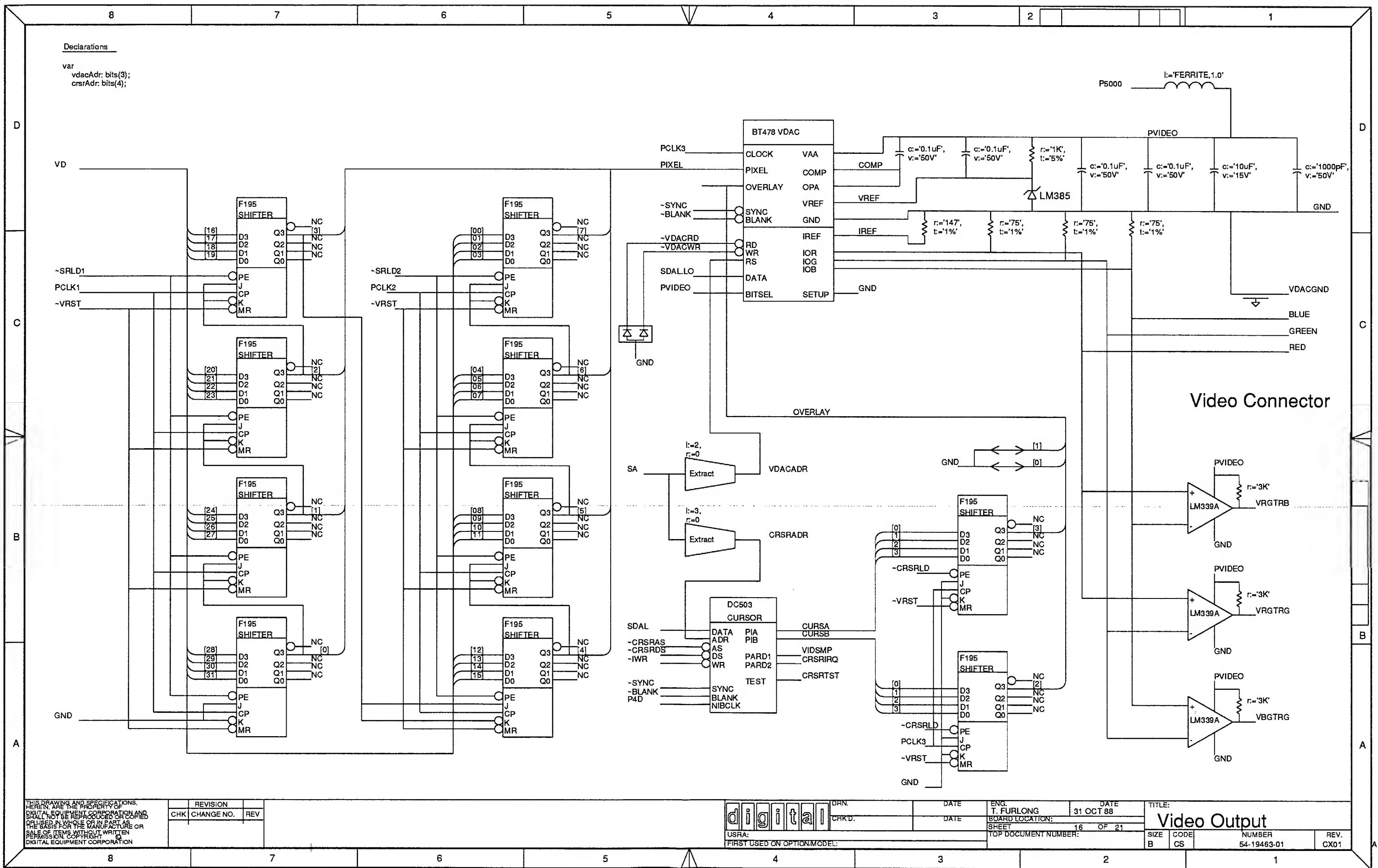
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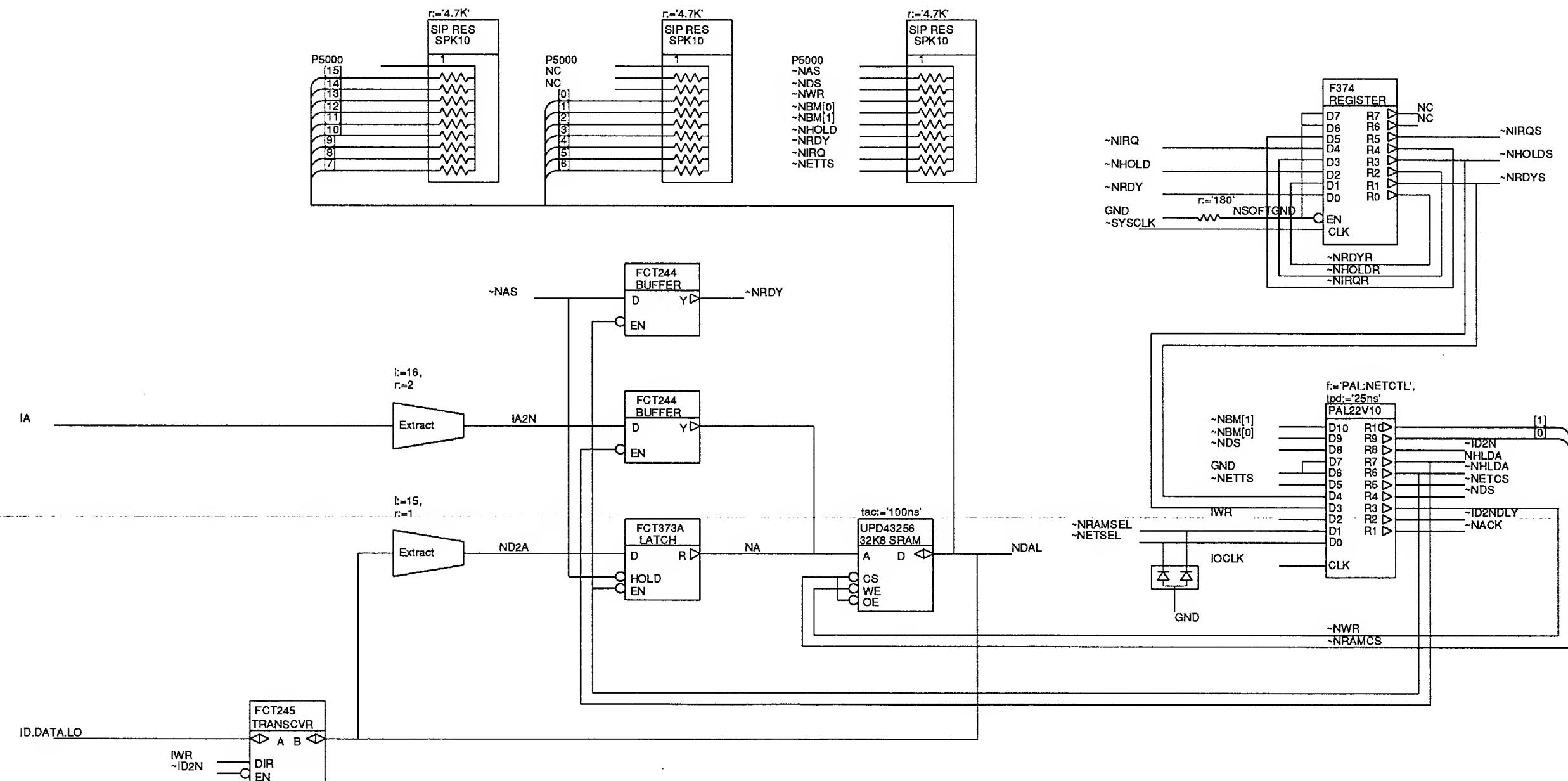
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TITLE: **Video Control**



Declarations

```
var
  ia2n: bits(15);
  nd2a: bits(15);
```



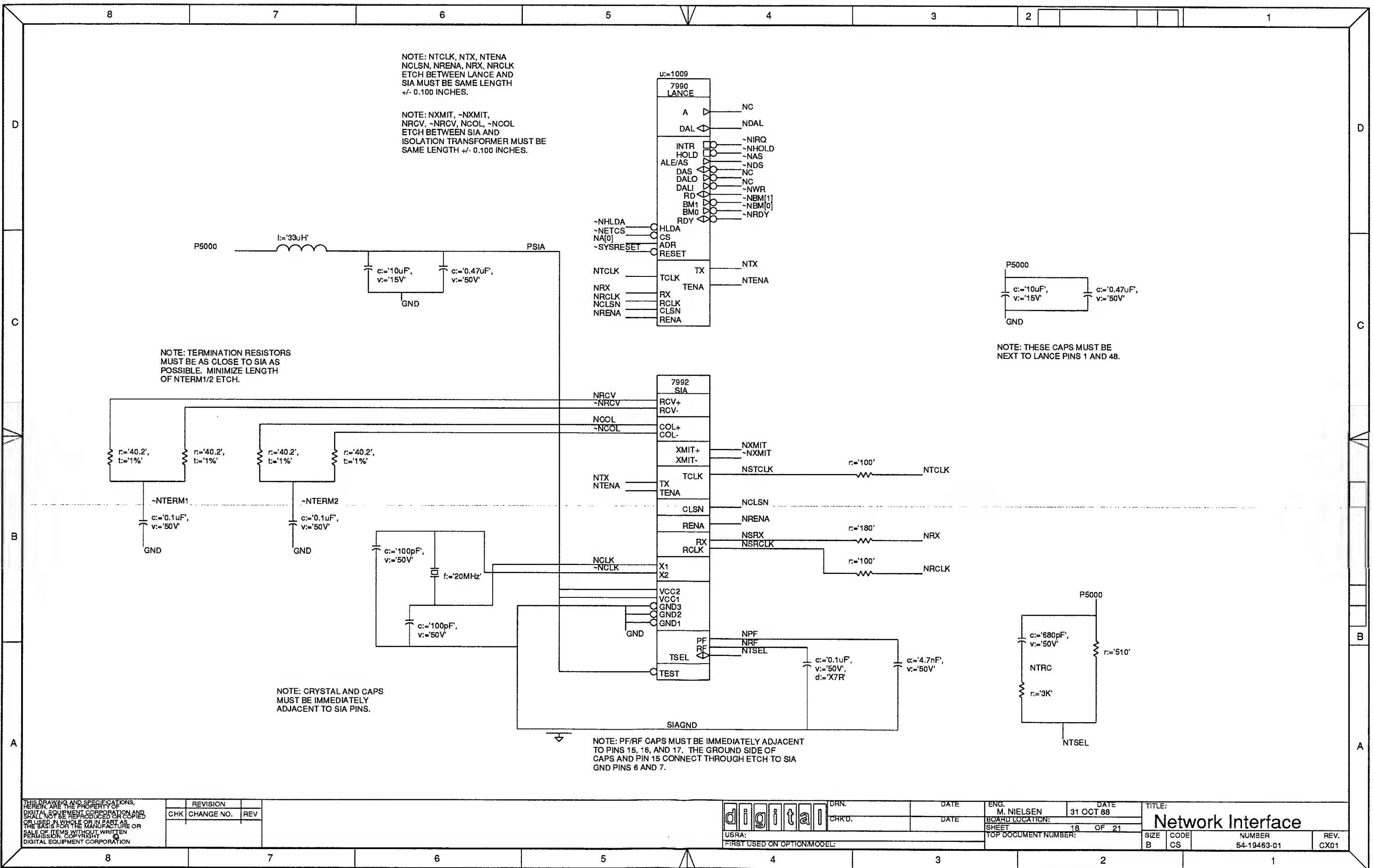
NOTE: NET BUFFER IS 32K 16-BIT WORDS. BUFFER IS
16-BIT-ALIGNED TO LANCE, 32-BIT-ALIGNED TO CPU.
BUFFER HAS 8-BIT ACCESS TO LANCE, 16-BIT ACCESS
TO CPU. CPU IS DEFAULT MASTER OF NDAL, AND IS
STALLED FOR LANCE-CSR OR BUFFER ACCESS IF LANCE
DMA IS IN PROGRESS.

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8 7 6 5 4 3 2 1

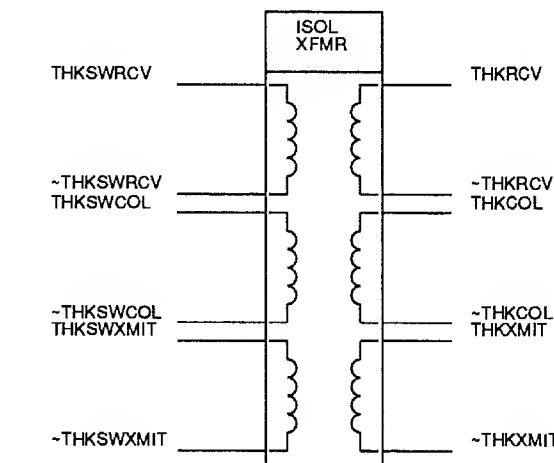
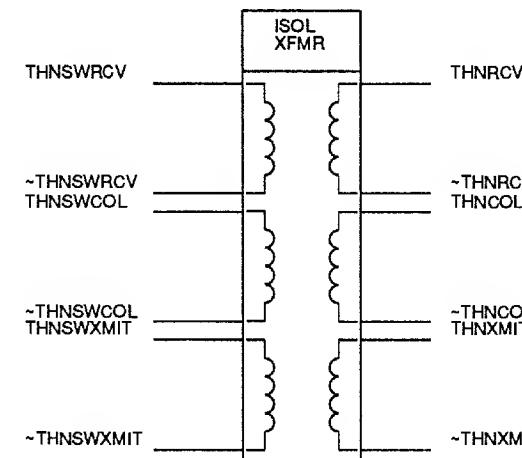
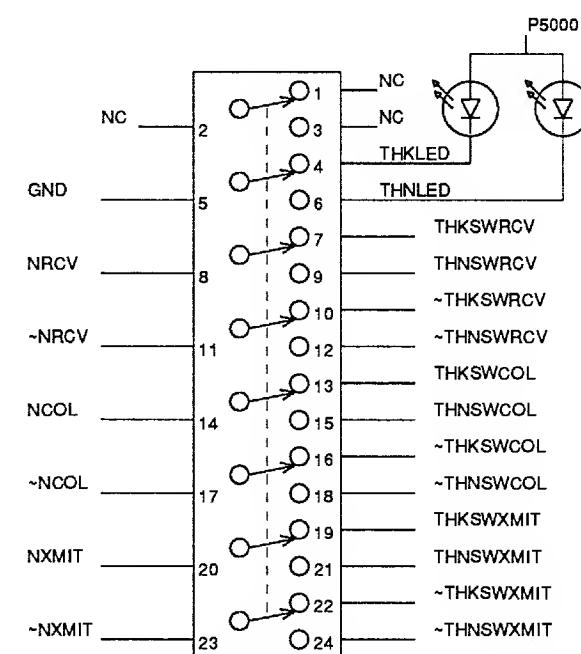
CHK	REVISION	CHANGE NO.	REV	DRN.	DATE	ENG.	DATE	TITLE:
1				digital	CHKD.	M. NIELSEN	31 OCT 88	Network Buffer
					DATE	BOARD LOCATION:		
						SHEET	17 OF 21	

USRA:
FIRST USED ON OPTION/MODEL:
TOP DOCUMENT NUMBER:
SIZE B CODE CS NUMBER 54-19463-01 REV. CX01



8 7 6 5 4 3 2 NC 1

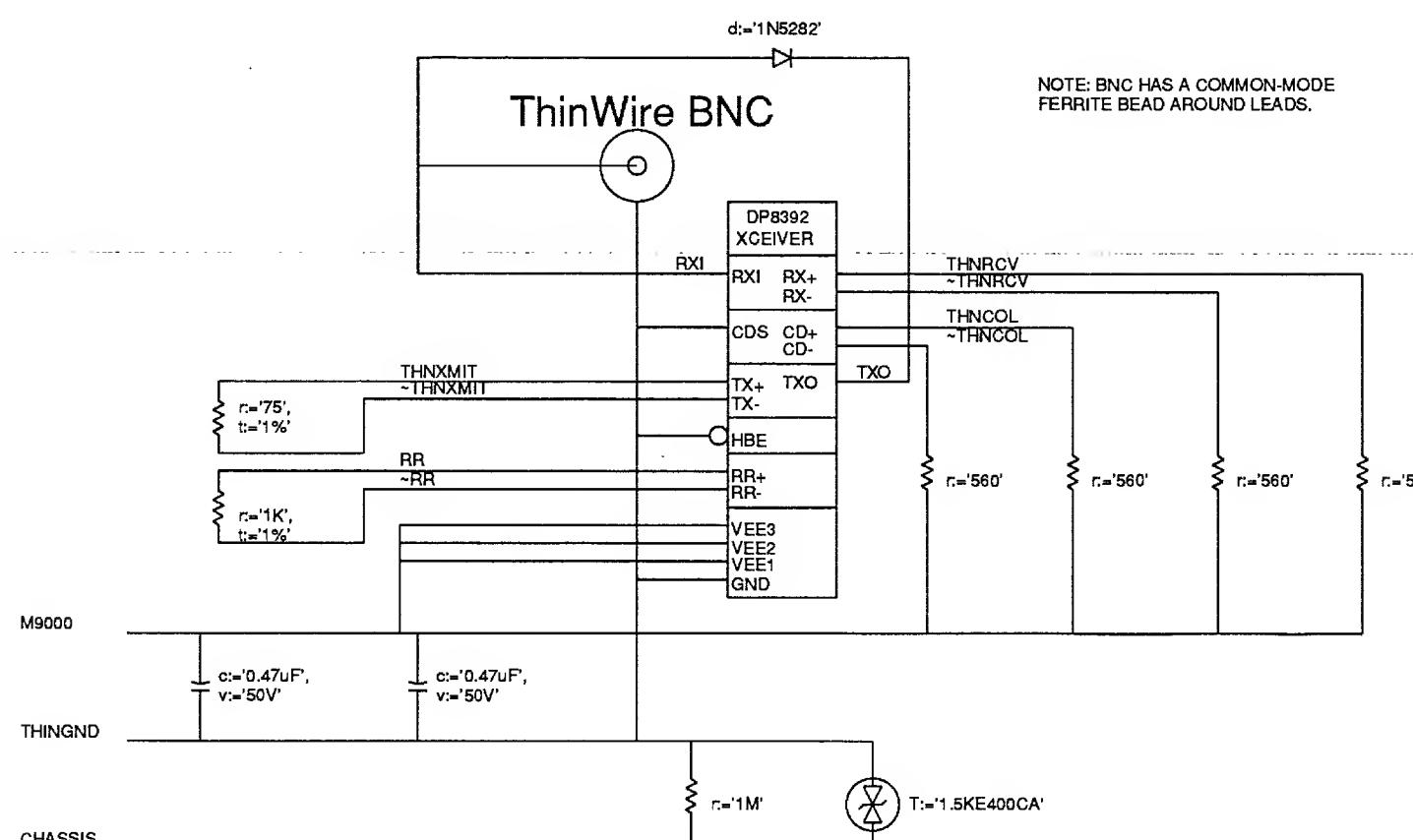
Thin/Thick Selector Switch



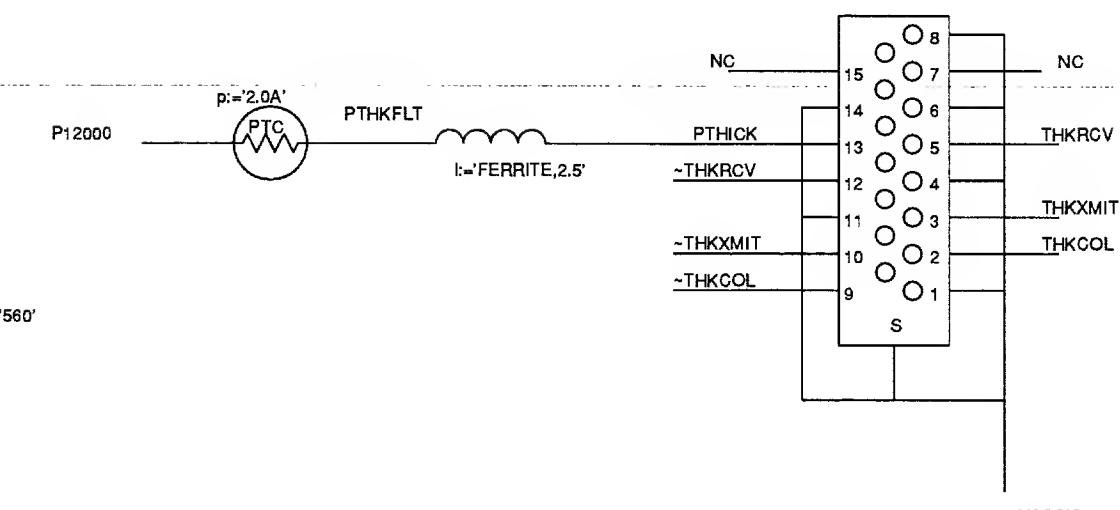
d:=1N5282

ThinWire BNC

NOTE: BNC HAS A COMMON-MODE FERRITE BEAD AROUND LEADS.



ThickWire Connector



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CHK	REVISION	CHANGE NO.	REV



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BOARD LOCATION:
SHEET 19 OF 21
TOP DOCUMENT NUMBER:

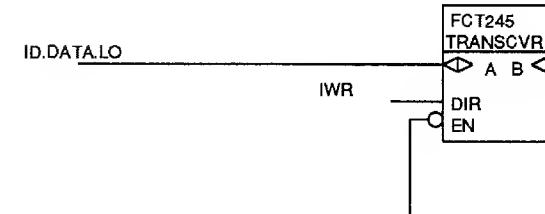
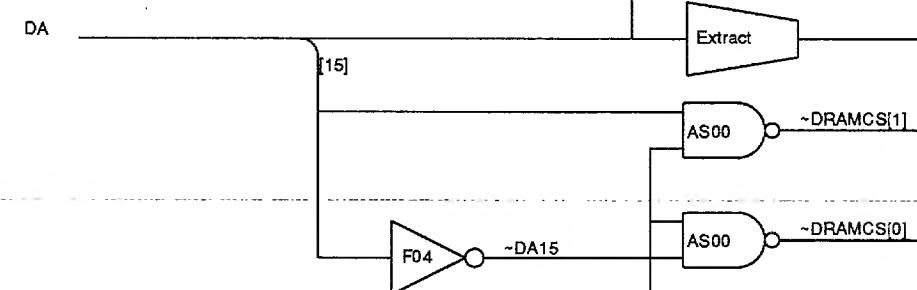
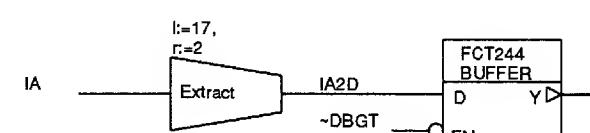
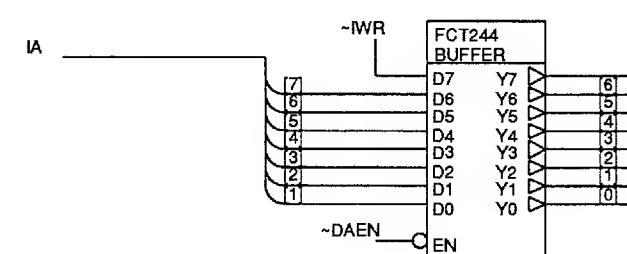
ENG.
M. NIELSEN
31 OCT 88
CODE
NUMBER
54-19463-01
REV.
CX01

TITLE:
Ethernet Transceiver
SIZE
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CS
NUMBER
54-19463-01
REV.
CX01

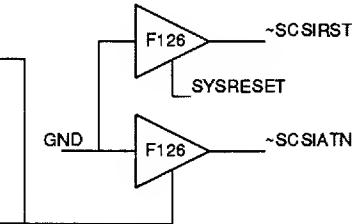
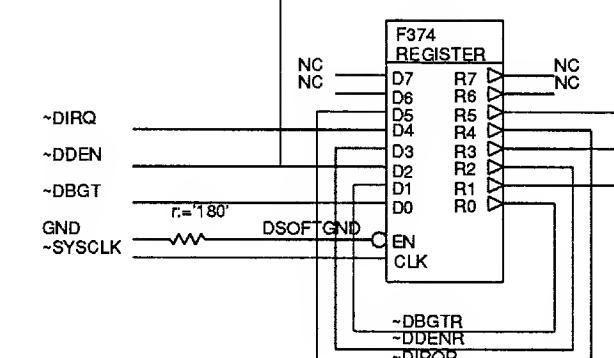
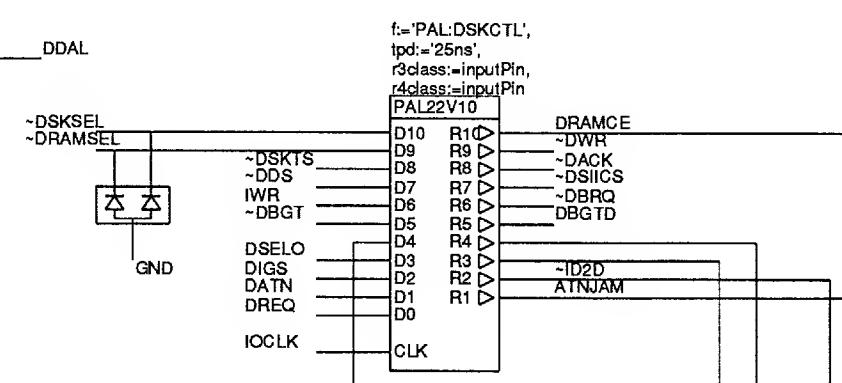
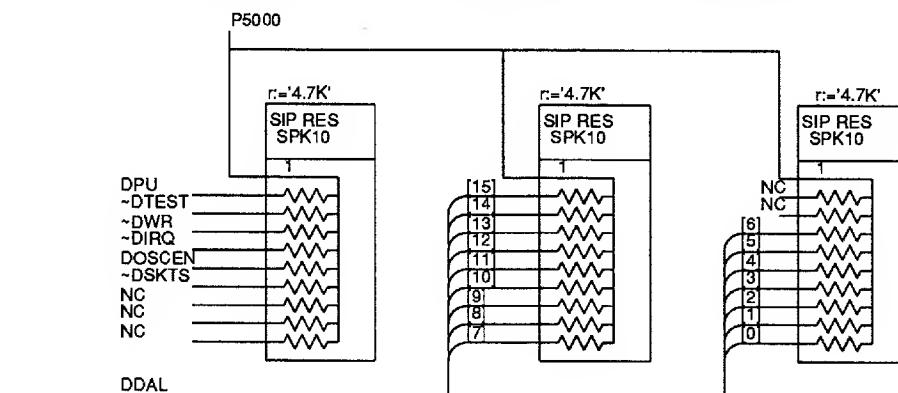
8 7 6 5 4 3 2 1

Declarations

```
var
ia2d: bits(16);
dba: bits(15);
```



NOTE: DISK BUFFER IS 64K 16-BIT WORDS. BUFFER IS
16-BIT-ALIGNED TO SII, 32-BIT-ALIGNED TO CPU.
BUFFER HAD 16-BIT ACCESS TO SII AND CPU. SII IS
DEFAULT MASTER OF DDAL. CPU IS STALLED FOR SII-CSR
OR BUFFER ACCESS IF SII DMA IS IN PROGRESS.



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CHK	REVISION	CHANGE NO.	REV
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digital	DRN:	DATE:	ENG.:	DATE:	TITLE:
	CHKD.		M. NIELSEN	31 OCT 88	Disk Buffer
USRA:		DATE:		BOARD LOCATION:	
FIRST USED ON OPTION/MODEL:				SHEET 20 OF 21	TOP DOCUMENT NUMBER:
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					CODE: CS
					NUMBER: 54-19463-01
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